

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2585	pipeline near2 stall	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 10:32
L2	371	register near2 scoreboard	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 10:32
L4	1	l2 same ((track\$3 or maintain\$3) with l1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 10:36
L5	20	l2 same (l1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 10:36
L6	13562	instruction with schedul\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 10:33
L7	7	l5 and L6	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 10:37
L8	2	l2 same ((track\$3 or maintain\$3) with (stall or latency or delay))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 10:36
L9	29	l2 with (stall or latency or delay)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 10:36

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L10	10	15 and L9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 10:37
L12	14080	(reorder\$3 or schedul\$3) with interval	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 11:21
L13	62	112 and L6 and (data near2 dependency)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 11:22
S1	13562	instruction with schedul\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 08:14
S2	4588	dependency with range	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 08:17
S3	43	S1 and S2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 08:15
S4	0	scheudul\$3 with range	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 08:17
S5	297	reorder\$3 with range	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 08:21

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S6	15	S1 and S5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 08:17
S7	162689	(rearrange\$3 or mov\$3 or reschedul\$3) with range	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 09:15
S8	255	S1 and S7	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 08:22
S9	10	S8 and (data near2 dependency)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 11:21
S10	1230	S1 and (data near2 dependency)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 09:10
S11	496	S10 and range	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 09:09
S12	536	(range or extend or scope or valid)same (data near2 dependency)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 09:12
S13	119	S1 and S12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 09:12

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S14	314	(range or extend or scope)same (data near2 dependency)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 09:13
S15	36	S1 and S14	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 09:12
S16	20	(determin\$3 with range)same (data near2 dependency)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 09:14
S17	3	S1 and S16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 09:14
S18	189857	(reorder\$3 or rearrange\$3 or mov\$3 or movable or reschedul\$3) with range	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 11:19
S19	11176	S18 with determin\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 09:15
S20	22	S1 and S19	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/11/09 09:16


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[Software pipelining: an effective **scheduling** technique for VLIW machines - all 15 versions »](#)

M Lam - Proceedings of the ACM SIGPLAN 1988 conference on ..., 1988 - portal.acm.org
 ... of registers; otherwise, the resulting **data** memory bottleneck ... particular branch then the **instruction** simply can ... line sequences in the **scheduling** process makes ...

[Cited by 680](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Global **instruction scheduling** for superscalar machines](#)

D Bernstein, M Rodeh - Proceedings of the ACM SIGPLAN 1991 conference on ..., 1991 - portal.acm.org

... **data dependence** information summarized in a Program **Dependence** Graph, to move **instructions** well beyond ... This novel **scheduling** framework is based on ...

[Cited by 162](#) - [Related Articles](#) - [Web Search](#)

[Accurate static branch prediction by value **range** propagation - all 8 versions »](#)

JRC Patterson - Proceedings of the ACM SIGPLAN 1995 conference on ..., 1995 - portal.acm.org

... useful information, but for global **instruction scheduling** and **instruction** cache optimizations the accuracy of the ... method for obtaining branch prediction **data**. ...

[Cited by 77](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Dynamic **instruction scheduling** and the Astronautics ZS-1 - all 7 versions »](#)

JE Smith - Computer, 1989 - ieeexplore.ieee.org

... then be automati- cally routed via a common **data** bus to ... Control **dependencies**. ... For static code **scheduling**, **instruction** sequences are often divided into basic ...

[Cited by 111](#) - [Related Articles](#) - [Web Search](#)

[Region **scheduling**: an approach for detecting and redistributingparallelism - all 8 versions »](#)

R Gupta, ML Soffa - Software Engineering, IEEE Transactions on, 1990 - ieeexplore.ieee.org

... MIPS, a pipelined re- duced-**instruction**-set processor ... program representation used in region **scheduling** is an ... can be made or not, **data dependency** information is ...

[Cited by 123](#) - [Related Articles](#) - [Web Search](#)

[Limits on multiple **instruction** issue - all 5 versions »](#)

MD Smith, M Johnson, MA Horowitz - Proceedings of the third international conference on ..., 1989 - portal.acm.org

... in fa.c- tors we felt, would limit performance: **data**. ... r processor model, the instruc- tion **scheduler** performs the ... ed in- structions from the **instruction** window ...

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[The Multicluster Architecture: Reducing Processor Cycle Time Through Partitioning - all 21 versions »](#)

KI Farkas, P Chow, NP Jouppi, Z Vranesic - International Journal of Parallel Programming, 1999 - Springer

... a register and hence a cluster for a **data** value is ... That is, prepass **scheduling** must

be used. ... for one **instruction** can affect those made for other **instructions**. ...

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HIGHLY CONCURRENT SCALAR PROCESSING' - [all 4 versions »](#)

PYT Hsu, ES Davidson - [portal.acm.org](#)

... the program for the purpose of **instruction scheduling** is the ... **Instructions** are shown as nodes in the graph, labeled ... Arcs in the graph represent **data** or control ...

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The Metaflow Architecture - [all 5 versions »](#)

V Popesco, M Schultz, J Spracklen, G Gibson, B ... - [doi.ieeecs.org](#)

... on a unified structure, the DRIS (deferred-**scheduling**, register-renaming ... of writing the result of the **instruction** execution into ... **Data dependency** and stalling. ...

[Cited by 115](#) - [Related Articles](#) - [Web Search](#)

Power analysis and minimization techniques for embedded DSP software - [all 5 versions »](#)

MTC Lee, V Tiwari, S Malik, M Fujita - Very Large Scale Integration (VLSI) Systems, IEEE ..., 1997 - [ieeexplore.ieee.org](#)

... The **data** in the table also suggests that choosing an ... A **scheduling** algorithm for doing so is described in ... 2) Base/Overhead Cost of Packed **Instruction**: Table V ...

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Software pipelining: an effective scheduling technique for VLIW machines - all 15 versions »

M Lam - Proceedings of the ACM SIGPLAN 1988 conference on ..., 1988 - portal.acm.org
... by the example, inter-iteration **data dependencies** may introduce ... for a target initiation **interval** is the ... is derived from the **list scheduling** algorithm used in ...

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Introduction to the scheduling problem - all 20 versions »

RA Walker, S Chaudhuri - IEEE Design & Test of Computers, 1995 - doi.ieeeecs.org
... immediate successor of o i . We represent this **data dependency** during the ... and its mobility is also 1; and the **schedule interval** for operation 4 ... **list scheduling**. ...

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Path-based scheduling for synthesis - all 3 versions »

R Camposano - Computer-Aided Design of Integrated Circuits and Systems, ..., 1991 - ieeexplore.ieee.org

... are scheduled AFAP independently, according to the **data-flow** constraints ... The result is a DAG, and a **list** of removed ... and finally an AFAP **schedule** for each path. ...

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A formal approach to the scheduling problem in high level synthesis - all 4 versions »

CT Hwang, JH Lee, YC Hsu - Computer-Aided Design of Integrated Circuits and Systems, ..., 1991 - ieeexplore.ieee.org

... can join several operations with **data dependencies** in one ... system for synthesizing a pipelined **data** path ... It uses a modified **list scheduling** technique to **schedule** ...

Cited by 200 - [Related Articles](#) - [Web Search](#)

Lifetime-sensitive modulo scheduling - all 8 versions »

RA Huff - ACM SIGPLAN Notices, 1993 - portal.acm.org

... code, (instead of a loop), a **list-scheduling** compiler can ... To software pipeline a loop, a **scheduler** must handle cyclic **data dependencies**, which arise ...

Cited by 141 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

LEneS: task scheduling for low-energy systems using variable supply voltage processors - all 9 versions »

F Gruian, K Kuchcinski - Proceedings of the 2001 conference on Asia South Pacific ..., 2001 - portal.acm.org

... order of task execution, which is imposed by the various **data dependencies**. ... 5), is based on **list-scheduling** with a energy sensitive ... A. The Energy of a **Schedule** ...

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HIGHLY CONCURRENT SCALAR PROCESSING' - all 4 versions »

PYT Hsu, ES Davidson - portal.acm.org

... Figure 8 shows the **dependency** graph for the ongoing ... Arcs in the graph represent **data** or control de ... However, it is well known that **list scheduling** techniques [19 ...

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Some **scheduling** techniques and an easily schedulable horizontal architecture for high performance ... - all 3 versions »

BR Rau, CD Glaeser - ACM SIGMICRO Newsletter, 1981 - portal.acm.org

... the approach is to maintain a **list** of "ready ... remains to be shown that this **schedule** is correct ... constraint are not even considered, no **data dependencies** can have ...

Cited by 298 - Related Articles - Web Search

Control-flow versus **data-flow-based scheduling**: combining both approaches in an adaptive **scheduling** ... - all 6 versions »

RA Bergamaschi, S Raje, I Nair, L Trevillyan - Very Large Scale Integration (VLSI) Systems, IEEE ..., 1997 - ieeexplore.ieee.org

... BERGAMASCHI et al.: CONTROL-FLOW VERSUS **DATA-FLOW-BASED SCHEDULING** ... the target po-

sitions (Tp(op)) for all constraint-generating operations in **list** CGopl ...

Cited by 36 - Related Articles - Web Search - Library Search

Iterative modulo **scheduling**: an algorithm for software pipelining loops - all 4 versions »

BR Rau - Proceedings of the 27th annual international symposium on ..., 1994 - portal.acm.org

... specified as a **list** of resources and the attendant ... When performing **scheduling** with a realistic machine model ... **data** structure similar to the reservation table is ...

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